



Global Communication Semiconductors, LLC

Corporate & Foundry

23155 Kashiwa Court, Torrance, CA. 90505

Telephone: 310-530-7274 Fax: 310-517-8200

Website: www.gcsincorp.com

Job Title: **CAD / Tapeout Engineer**
Job Req: #0631
Department: Foundry Services
Status: Full-Time/Exempt
Shift: Day

Job Description

- Solid understanding of tapeout cycle.
- Work with customer and process team to support tapeout process.
- Assemble PCMs. Able to do data management, such as version control.
- Mask procurement and PO requisition. Schedule, track, and document orders.
- Create Wafer-Assembly-Drawing (WAD). Capture and document process requirements.
- Provide DRC services and support customers on DRC feedback.
- Internal / Customer Support: Perform layout per requirements.
- Work with Production Control team for lot start.
- Support process team for layout illustration.

Job Requirements

- BA or BS degree. EE preferred
- Layout: 3+ years' experience in Cadence Virtuoso, ADS, MWO, or dw-2000
- Experience in semiconductor or EDA industry
- Script experience. AWK, Perl, Ruby, SKILL or C++
- Microsoft OS, UNIX / Linux
- Strong knowledge of Microsoft Office (Word, Excel, PowerPoint)
- Proficient in typing
- Ability to learn and perform in a fast-paced environment
- Excellent interpersonal skills
- Must have good written and verbal communication skills